

1 ABSTRACT OF THE DISCLOSURE

2 An active termination circuit is mounted in a memory circuit and includes a
3 termination resistor which provides a termination resistance for the memory circuit,
4 and a control circuit which receives an externally supplied active termination control
5 signal, and which selectively switches on and off the termination resistor in
6 response to the active termination control signal. The control circuit includes a
7 synchronous input buffer and an asynchronous input buffer which each receive the
8 active termination control signal, and a switching circuit which selectively outputs an
9 output of said synchronous input buffer or an output of said asynchronous input
10 buffer according to an operational mode of the memory circuit. The output of the
11 switching circuit controls an on/off state of said termination resistor.